

Notice of Allowability	Application No.	Applicant(s)	
	09/589,795	ARNDT, RICHARD LOUIS	
	Examiner Lilian Vo	Art Unit 2127	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTO-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 8/26/04.
2. The allowed claim(s) is/are 1 - 19.
3. The drawings filed on 13 October 2000 are accepted by the Examiner.
4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All
 - b) Some*
 - c) None
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached 1) hereto or 2) to Paper No./Mail Date _____.
 - (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).**
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)
2. Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. Notice of Informal Patent Application (PTO-152)
6. Interview Summary (PTO-413),
Paper No./Mail Date 10222004.
7. Examiner's Amendment/Comment
8. Examiner's Statement of Reasons for Allowance
9. Other _____.

MENGBAL TAN
SUPERVISORY PATENT EXAMINER
 TECHNOLOGY CENTER 2100
 Lilian Vo
 Examiner
 Art Unit: 2127

EXAMINER'S AMENDMENT

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
2. Authorization for this examiner's amendment was given in a telephone interview with Lisa Yociss on 10/26/2004.
3. The application has been amended as follows:
 - i) **Claim 19:**

Line 6, replace "said set of" with -- the --;
Line 8, replace "said set of" with -- the --;
Line 17, delete "a";
Line 18, delete "a";
 - ii) **Replace claims 1, 4, 7, 10, 13 and 16:**

Claim 1. (Currently amended) A method for mediating address translation in a logically partitioned data processing system having a set of logical partitions with an

operating system assigned to each logical partition within the set of logical partitions, the method comprising:

associating a different contiguous range of virtual address pages of virtual addresses with each one of said set of logical partitions;

assigning a noncontiguous group of page frames of physical addresses to each one of said different contiguous range of virtual address pages, each one of said set of logical partitions being assigned a different noncontiguous group of page frames;

wherein the physical addresses are addresses corresponding to physical location of resources within the logically partitioned data processing system;

receiving from an operating system within a logical partition from the set of logical partitions a request to access a physical resource;

responsive to a determination that the physical resource has been allocated to the logical partition, selectively modifying an address translation table to allow access to the physical resource by the operating system.

Claim 4. (Currently amended) The method as recited in claim 1, wherein the address translation table comprises a table of virtual addresses with corresponding physical addresses, wherein the virtual addresses are addresses utilized by the operating system.

Claim 7. (Currently amended) A computer program product in a computer readable media for use in a logically partitioned data processing system for mediating

address translation in a logically partitioned data processing system having a set of logical partitions with an operating system assigned to each logical partition in the set of logical partitions, the computer program product comprising:

instructions for associating a different contiguous range of virtual address pages of virtual addresses with each one of said set of logical partitions;

instructions for assigning a noncontiguous group of page frames of physical addresses to each one of said different contiguous range of virtual address pages, each one of said set of logical partitions being assigned a different noncontiguous group of page frames;

wherein the physical addresses are addresses corresponding to physical location of resources within the logically partitioned data processing system;

instructions for receiving from an operating system within a logical partition from the set of logical partitions a request to access a physical resource;

instructions, responsive to a determination that the physical resource has been allocated to the logical partition, for selectively modifying an address translation table to allow access to the physical resource by the operating system.

Claim 10. (Currently amended) The computer program product as recited in claim 7, wherein the address translation table comprises a table of virtual addresses with corresponding physical addresses, wherein the virtual addresses are addresses utilized by the operating system.

Claim 13 (Currently amended) A system for use in a logically partitioned data processing system for mediating address translation in a logically partitioned data processing system having a set of logical partitions with an operating system assigned to each logical partition in the set of logical partitions, the system comprising:

a different contiguous range of virtual address pages of virtual addresses being associated with each one of said set of logical partitions;

a noncontiguous group of page frames of physical addresses being assigned to each one of said different contiguous range of virtual address pages, each one of said set of logical partitions being assigned a different noncontiguous group of page frames;

wherein the physical addresses are addresses corresponding to physical location of resources within the logically partitioned data processing system;

first means for receiving from an operating system within a logical partition from the set of logical partitions a request to access a physical resource;

second means, responsive to a determination that the physical resource has been allocated to the logical partition, for selectively modifying an address translation table to allow access to the physical resource by the operating system.

Claim 16. (Currently amended) The system as recited in claim 13, wherein the address translation table comprises a table of virtual addresses with corresponding physical addresses, wherein the virtual addresses are addresses utilized by the operating system.

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4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Lilian Vo whose telephone number is 571-272-3774. The examiner can normally be reached on Monday - Thursday, 7:30am - 5pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Meng-Ai An can be reached on 571-272-3756. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Lilian Vo
Examiner
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lv
October 27, 2004


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